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CONFIRMATION NO. 2083

Bib Data Sheet

SERIAL NUMBER 10/061,504	FILING DATE 02/01/2002  RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. 10010205-1
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## APPLICANTS

Robert M.R. Neff, Palo Alto, CA;

\*\* CONTINUING DATA *None, Unk.*\*\* FOREIGN APPLICATIONS *None, Unk.*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/27/2002

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 12	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Initials				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i>				

## ADDRESS

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## TITLE

Interleaved clock signal generator having serial delay and ring counter architecture

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